

Fig. 2
overall
logic

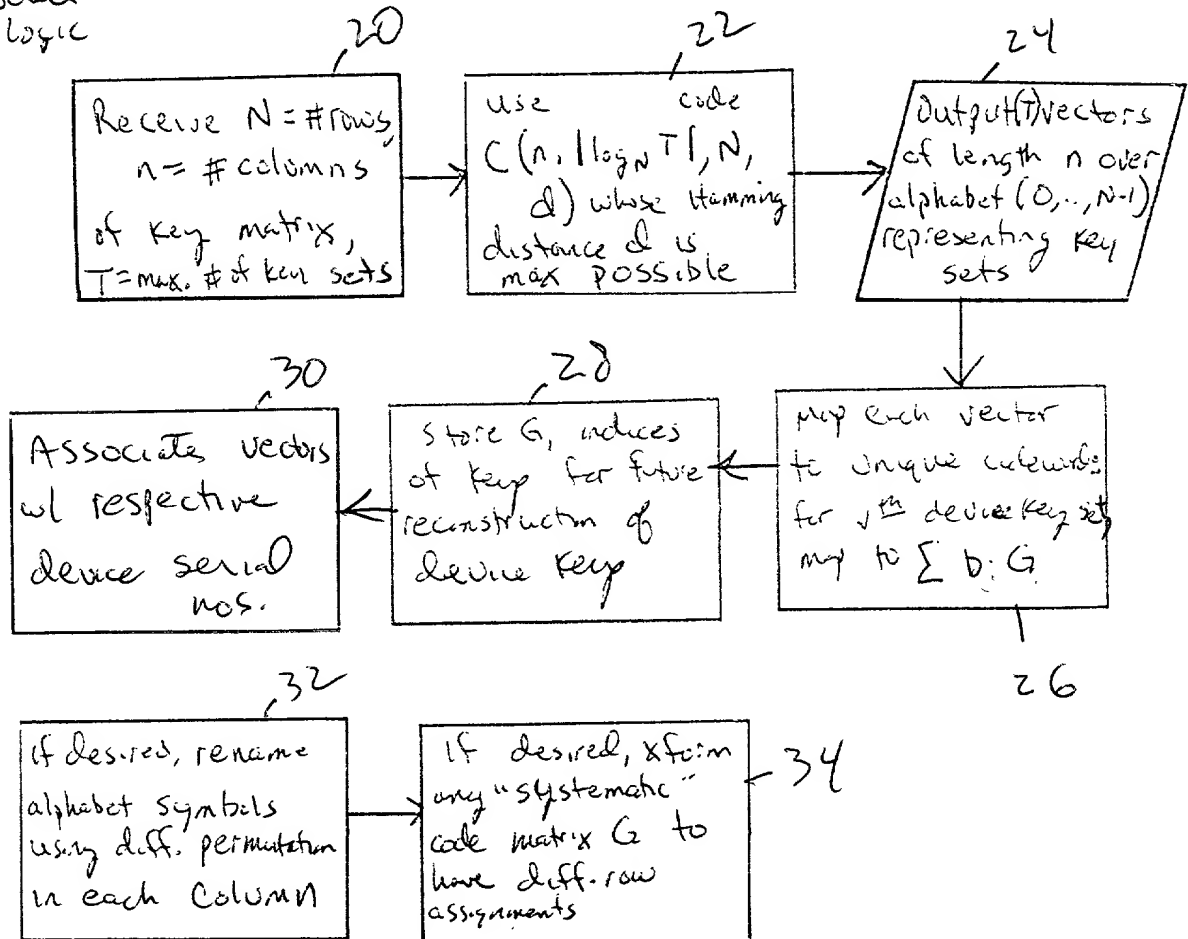


Fig. 3
optional
enhancements